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XA-9582
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Yoshihiro IKEDA et al.

Appln. No.: 09/987,884

Group Art Unit: 2813

Filed: November 16, 2001

Examiner: L. Schillinger

For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
MANUFACTURING METHOD THEREOF

* * *

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Responding to the Office Action mailed November 29,
2002 please amend the above-identified application as
indicated below.

IN THE ABSTRACT OF THE DISCLOSURE:

Please substitute the abstract on the appended
separate sheet for the abstract filed with this
application.

TECHNOLOGY CENTER 2800

JUN-2 2003

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